

IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE

Membership Publications/Services Standards Conferences Careers/Jobs

IEEE Xplore
RELEASE 1.4Welcome
United States Patent and Trademark Office[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#) [Quick Links](#)

» Search

Welcome to IEEE Xplore

☐ Home☐ What Can I Access?☐ Log-out

Tables of Contents

☐ Journals & Magazines☐ Conference Proceedings☐ Standards

Search

☐ By Author☐ Basic☒ Advanced

Member Services

☐ Join IEEE☐ Establish IEEE Web Account☐ Access the IEEE Member Digital Library Print Format

Your search matched **12** of **987057** documents.
Results are shown **15** to a page, sorted by **publication year** in **descending** order.

Results:Journal or Magazine = **JNL** Conference = **CNF** Standard = **STD****1 Efficient coding of linear predictive coefficients for wideband speech***Magrath, A.J.; Sandler, M.B.;*

Circuits and Systems, 1998. ISCAS '98. Proceedings of the 1998 IEEE International Symposium on , Volume: 4 , 31 May-3 June 1998

Page(s): 190 -193 vol.4

[\[Abstract\]](#) [\[PDF Full-Text \(312 KB\)\]](#) **IEEE CNF****2 Encoding hidden data channels in sigma-delta bitstreams***Magrath, A.J.; Sandler, M.B.;*

Circuits and Systems, 1998. ISCAS '98. Proceedings of the 1998 IEEE International Symposium on , Volume: 1 , 31 May-3 June 1998

Page(s): 385 -388 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(324 KB\)\]](#) **IEEE CNF****3 A sigma-delta modulator topology with high linearity***Magrath, A.J.; Sandler, M.B.;*

Circuits and Systems, 1997. ISCAS '97., Proceedings of 1997 IEEE International Symposium on , Volume: 1 , 9-12 June 1997

Page(s): 53 -56 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(344 KB\)\]](#) **IEEE CNF****4 Parallel interconnection of cascaded subfilters: improved performance at high order***Waters, M.; Price, M.; Sandler, M.B.; Magrath, A.J.;*

Circuits and Systems, 1997. ISCAS '97., Proceedings of 1997 IEEE International Symposium on , Volume: 4 , 9-12 June 1997

Page(s): 2216 -2219 vol.4

[\[Abstract\]](#) [\[PDF Full-Text \(280 KB\)\]](#) **IEEE CNF****5 Design and implementation of a FPGA sigma-delta power DAC***Magrath, A.J.; Clark, I.G.; Sandler, M.B.;*

Signal Processing Systems, 1997. SIPS 97 - Design and Implementation., 1997

Workshop on , 3-5 Nov. 1997

Page(s): 511 -521

[\[Abstract\]](#) [\[PDF Full-Text \(492 KB\)\]](#) **IEEE CNF**

6 Audio coding for mobile multimedia communications

Sandler, M.B.; Kudumakis, P.; Magrath, A.J.;

Mobile Multimedia Communications (Digest No. 1996/248), IEE Colloquium on Future of , 6 Dec. 1996

Page(s): 11/1 -11/9

[\[Abstract\]](#) [\[PDF Full-Text \(744 KB\)\]](#) **IEE CNF**

7 Performance enhancement of sigma-delta modulator D-A converter: non-linear techniques

Magrath, A.J.; Sandler, M.B.;

Circuits and Systems, 1996. ISCAS '96., 'Connecting the World', 1996 IEEE International Symposium on , Volume: 2 , 12-15 May 1996

Page(s): 277 -280 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(316 KB\)\]](#) **IEEE CNF**

8 Hybrid pulse width modulation/sigma-delta modulation power digital-to-analogue converter

Magrath, A.J.; Sandler, M.B.;

Circuits, Devices and Systems, IEE Proceedings [see also IEE Proceedings G-Devices and Systems] , Volume: 143 Issue: 3 , June 1996

Page(s): 149 -156

[\[Abstract\]](#) [\[PDF Full-Text \(804 KB\)\]](#) **IEE JNL**

9 Non-linear deterministic dithering of sigma-delta modulators

Magrath, A.J.; Sandler, M.B.;

Oversampling and Sigma-Delta Strategies for DSP, IEE Colloquium on , 23 Nov. 1996

Page(s): 2/1 -2/6

[\[Abstract\]](#) [\[PDF Full-Text \(384 KB\)\]](#) **IEEE CNF**

10 Power digital-to-analogue conversion using a sigma-delta modulator controlled limit cycles

Magrath, A.J.; Sandler, M.B.;

Electronics Letters , Volume: 31 Issue: 4 , 16 Feb. 1995

Page(s): 251 -253

[\[Abstract\]](#) [\[PDF Full-Text \(216 KB\)\]](#) **IEE JNL**

11 Resolution enhancement and dither of sigma-delta modulator digital-to-analogue converters

Magrath, A.J.; Sandler, M.B.;

Electronics Letters , Volume: 31 Issue: 18 , 31 Aug. 1995
Page(s): 1540 -1542

[\[Abstract\]](#) [\[PDF Full-Text \(240 KB\)\]](#) **IEE JNL**

12 Efficient dithering of sigma-delta modulators with adaptive bit flip
Magrath, A.J.; Sandler, M.B.;

Electronics Letters , Volume: 31 Issue: 11 , 25 May 1995
Page(s): 846 -847

[\[Abstract\]](#) [\[PDF Full-Text \(156 KB\)\]](#) **IEE JNL**

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#)
[Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#)
[No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2003 IEEE — All rights reserved